

## AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

### Listing of Claims

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1. **(Currently Amended)** A method of accessing state from a configurable processor, the method comprising:
- transmitting, using a debugger ~~software application~~, a state-accessing instruction stream to an interpreting agent ~~in the configurable processor~~, the ~~software~~ interpreting agent being capable of interpreting that stream; and
- causing, using the state-accessing instruction stream, the interpreting agent to return the state of the processor to the debugger ~~software application~~.
- a 2. (Original) A method as in claim 1 where the interpreting agent is a monitor program.
3. (Original) A method as in claim 1 where the interpreting agent is an instruction insertion server.
4. (Original) A method as in claim 1 where the interpreting agent is an architectural simulator.
5. **(Currently Amended)** A method as in claim 1, further comprising:
- reading, using the debugger ~~software application~~, information describing the configurable processor's state architecture; and
- generating, using the debugger ~~software application~~, the instruction stream based on the information.
6. (Original) A method as in claim 5 wherein the interpreting agent is a monitor program.

7. (Original) A method as in claim 5 wherein the interpreting agent is an instruction insertion server.

8. (Original) A method as in claim 5 wherein the interpreting agent is an architectural simulator.

9. (Currently Amended) A computer-readable storage medium storing therein a software program comprising ~~capable of:~~

software for generating a configurable processor from a user description of that processor ~~that also generates;~~ and

a debugger library for generating information necessary to describe save and restore instructions for state of the configurable processor based on the user description.

10. (Currently Amended) A computer-readable storage medium storing therein a ~~software library usable~~ debugger library for:

reading a description of save and restore state information of a configurable processor; and then

generating saving and restoring state instruction streams ~~therefrom~~ based on the description.

11. (Currently Amended) A medium as in claim 10 wherein the ~~software library~~ debugger library further comprises functionality for:

~~also can deal with~~ identifying interdependencies in state ~~to generate;~~ and generating a complete and correct save and restore sequence based on the interdependencies.

12. (Currently Amended) An instruction-insertion server comprising:

means for ~~that takes~~ retrieving system topology information of a chip containing multiple cores from a computer-readable file; and

means for to determine determining where elements are in a system described by the file.

13. (Currently Amended) A system for accessing state from a configurable processor, the system comprising:

a debugger ~~software application~~ which transmits a state-accessing instruction stream;  
an interpreting agent ~~in the configurable processor~~ which  
receives the instruction stream,  
interprets the instruction stream to access state of the configurable processor, and  
returns the accessed state of the configurable processor to the debugger ~~software~~  
application.

14. (Original) A system as in claim 13 where the interpreting agent is a monitor program.

15. (Original) A system as in claim 13 where the interpreting agent is an instruction insertion server.

16. (Original) A system as in claim 13 where the interpreting agent is an architectural simulator.

17. (Currently Amended) A system as in claim 13, wherein the debugger ~~software application~~ is adapted to:

read information describing the configurable processor's state architecture; and  
generate the instruction stream based on the information.

18. (Original) A system as in claim 17 wherein the interpreting agent is a monitor program.

19. (Original) A system as in claim 17 wherein the interpreting agent is an instruction insertion server.

20. (Original) A system as in claim 17 wherein the interpreting agent is an architectural simulator.

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